

DECLARATION

As the below-named inventors, we declare that:

Our residences, post office addresses, and citizenships are as stated below under our names.

We believe we are the original, first, and joint inventors of the invention entitled "METHOD AND SYSTEM FOR SELECTING COMPATIBLE PROCESSORS TO ADD TO A MULTIPROCESSOR COMMPUTER," which is described and claimed in the specification and claims of Patent Application No. 09/083,959, which we filed in the United States Patent and Trademark Office on May 22, 1998 and for which a patent is sought.

We have reviewed and understand the contents of the above-entitled specification, including the claims, as amended by any amendment specifically referred to herein (if any).

We acknowledge our duty to disclose information of which we are aware which is material to patentability and examination of this application in accordance with 37 C.F.R. § 1.56(a).

We further declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further, that these statements were made with the knowledge that the making of willfully false statements and the like is punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and may jeopardize the validity of any patent issuing from this patent application.

Robert Gentle
Robert Gentle

Date 8/12/98

Residence : City of ~~Nampa~~, County of ~~Canyon~~
~~Beaverton~~, ~~Washington~~
State of ~~Idaho~~ ~~Oregon~~

Citizenship : United States of America

P.O. Address : ~~1007 - 15th Avenue S.~~ 15272 SW Millikan Way, Apt. 422
~~Nampa, Idaho 83651~~ Beaverton, Oregon 97006

Travis Schaff
Travis Schaff

Date 7/21/98

Residence : City of Meridan, County of Ada
State of Idaho

Citizenship : United States of America

P.O. Address : 273 Spicewood Drive
Meridian, Idaho 83642

660082.477M
660082.477Mdecl

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Robert Gentile et al.
Application No. : 09/083,959
Filed : May 22, 1998
For : METHOD AND SYSTEM FOR SELECTING COMPATIBLE
PROCESSORS TO ADD TO A MULTIPROCESSOR
COMPUTER

Docket No. : 660082.477M

Box Missing Parts
Assistant Commissioner for Patents
Washington, DC 20231

ELECTION UNDER 37 C.F.R. §§ 3.71 AND 3.73 AND POWER OF ATTORNEY

Sir:

The undersigned, being Assignee of the entire interest in the above-identified application by virtue of an Assignment filed concurrently herewith, hereby elects, under 37 C.F.R. § 3.71, to prosecute the application to the exclusion of the inventors.

Assignee hereby appoints RICHARD W. SEED, Reg. No. 16,557; ROBERT J. BAYNHAM, Reg. No. 22,846; EDWARD W. BULCHIS, Reg. No. 26,847; GEORGE C. RONDEAU, JR., Reg. No. 28,893; DAVID H. DEITS, Reg. No. 28,066; WILLIAM O. FERRON, JR., Reg. No. 30,633; PAUL T. MEIKLEJOHN, Reg. No. 26,569; DAVID J. MAKI, Reg. No. 31,392; RICHARD G. SHARKEY, Reg. No. 32,629; DAVID V. CARLSON, Reg. No. 31,153; MAURICE J. PIRIO, Reg. No. 33,273; KARL R. HERMANN, Reg. No. 33,507; DAVID D. MCMASTERS, Reg. No. 33,963; ROBERT IANNUCCI, Reg. No. 33,514; MICHAEL J. DONOHUE, Reg. No. 35,859; CHRISTOPHER J. DALEY-WATSON, Reg. No. 34,807; STEVEN D. LAWRENZ, Reg. No. 37,376; ROBERT G. WOOLSTON, Reg. No. 37,263; ELLEN M. BIERMAN, Reg. No. 38,079; BRYAN A. SANTARELLI, Reg. No. 37,560; CAROL NOTTENBURG, Reg. No. 39,317; PAUL T. PARKER, Reg. No. 38,264; JOHN C. STEWART, Reg. No. 40,188; DAVID W. PARKER, Reg. No. 37,414; BRIAN G. BODINE, Reg. No. 40,520; FRANK ABRAMONTE, Reg. No. 38,066; E. RUSSELL TARLETON, Reg. No. 31,800;

FREDERICK M. FLIEGEL, Reg. No. 36,138; JAN C. L. MAXWELL, Reg. No. 41,181; THOMAS L. EWING, Reg. No. 34,328; CLIFTON G. GREEN, Reg. No. 41,044; KEVIN S. COSTANZA, Reg. No. 37,801; DALÉ C. BARR, Reg. No. 40,498; KEVIN S. ROSS, Reg. No. 42,116; PAUL F. RUSYN, Reg. No. 42,118; JOHN M. WECHKIN, Reg. No. 42,216; THOMAS E. LOOP, Reg. No. 42,810; STEPHEN J. ROSENMAN, Reg. No. 43,058; and BRIAN L. JOHNSON, Reg. No. 40,033, comprising the firm of SEED AND BERRY LLP, 6300 Columbia Center, Seattle, Washington 98104-7092; along with STEVEN P. ARNOLD, Reg. No. 33,354; and HOYT A. FLEMING III, Reg. No. 41,752, of Micron Electronics, Inc., 900 East Karcher Road, Nampa, Idaho 83687, as its attorneys to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. Please direct all telephone calls to **Maurice J. Pirio** at (206) 622-4900 and telecopies to (206) 682-6031.

Please direct all communications to:

Maurice J. Pirio, Esq.
Seed and Berry LLP
6300 Columbia Tower
701 Fifth Avenue
Seattle, Washington 98104-7092

Pursuant to 37 C.F.R. § 3.73, the undersigned duly authorized designee of Assignee certifies that the evidentiary documents have been reviewed, specifically the Assignment to MICRON ELECTRONICS, INC., filed concurrently herewith for recording, a copy of which is attached hereto, and certifies that to the best of my knowledge and belief, title remains in the name of the Assignee.

MICRON ELECTRONICS, INC.
ASSIGNEE

9-10-98
DATE

Hoyt A. Fleming III
HOYT A. FLEMING, III

Enclosure:

Copy of Assignment

wpn/660082/477M/Forms/Election

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
) Docket No.: 660082.477M
 Robert Gentile)
 Travis Schaff) Disclosure Number: 97.01144
)
)
 Serial No.: 09/083,959) Group Art Unit: 2784
)
 Filed: May 22, 1998)
)
 For: METHOD AND SYSTEM FOR)
 SELECTING COMPATIBLE)
 PROCESSORS TO ADD TO A)
 MULTIPROCESSOR COMPUTER)
 _____)

ASSIGNMENT:

 X Enclosed for recording
 Previously recorded
Date: _____
Reel: _____

FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned does hereby:

SELL, ASSIGN AND TRANSFER to **Micron Electronics, Inc.** (the "Assignee"), a corporation of Minnesota, having a place of business at 900 East Karcher Road, Nampa, Idaho 83687, the entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which was filed on May 22, 1998 and assigned Application No. 09/083,959 and is entitled "**METHOD AND SYSTEM FOR SELECTING COMPATIBLE PROCESSORS TO ADD TO A MULTIPROCESSOR COMPUTER**"; such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States of America;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such improvements to the Assignee;

WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or will be made to others by the undersigned, and that the full right to convey the same as herein expressed is possessed by the undersigned;

COVENANT that, when requested and at the expense of the Assignee, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all such improvements; execute all rightful oaths, declarations, assignments, powers of attorney and other papers; communicate to the Assignee all facts known to the undersigned relating to such improvements and the history thereof; and generally do everything possible which the Assignee shall consider desirable for securing, maintaining and enforcing proper patent protection for such improvements and for vesting title to such improvements in the Assignee;

TO BE BINDING on the heirs, assigns, representatives and successors of the undersigned and extend to the successors, assigns and nominees of the Assignee.

Robert Gentile Date: 8/12/98
Robert Gentile

OREGON
STATE OF ~~IDAHO~~)
) ss.
County of Washington

BEFORE ME, this 12th day of August, 1998, personally appeared the above-named individual, to me known to be the person who is described in and who executed the foregoing assignment instrument and acknowledged to me that executed the same of own free will for the purpose therein expressed.

SEAL



Peter J. Kim
(Notary) or Consular Officer
My Commission Expires June 21, 2002

Travis Schaff
Travis Schaff

Date: 7/21/98

STATE OF IDAHO)

) ss.

County of Lampson

BEFORE ME, this 21st day of July, 1998, personally appeared the above-named individual, to me known to be the person who is described in and who executed the foregoing assignment instrument and acknowledged to me that executed the same of own free will for the purpose therein expressed.

SEAL

Nancey Hammoncl
Notary or Consular Officer
My Commission Expires 6/12/03

660082.477Massign

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, DC 20231.

April 4, 2000
Date

Michele Coleman-Owens
Michele Coleman-Owens

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Robert Gentile et al. Attorney Docket No.: 660082.477M (500219.01)
Serial No. : 09/083,959 Group Art Unit : 2763
Filed : May 22, 1998 Examiner : Samuel Broda
Title : METHOD AND SYSTEM FOR SELECTING COMPATIBLE PROCESSORS TO
ADD TO A MULTIPROCESSOR COMPUTER

TRANSMITTAL FOR REVOCATION AND SUBSTITUTE POWER OF ATTORNEY

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Transmitted herewith and attached hereto as Addendum A is a true and correct copy of the Revocation and Substitute Power of Attorney executed February 7, 2000, in the above-identified application. The above-identified application is identified on Exhibit A.

Pursuant to 37 C.F.R. § 3.73, Paul A. Revis, duly authorized designee of Assignee, has certified that the evidentiary documents have been reviewed, specifically the Assignment to MICRON ELECTRONICS, INC., recorded and certified that to the best of his knowledge and belief, title remains in the name of the Assignee.

Respectfully submitted,

DORSEY & WHITNEY LLP

Kimton N. Eng
Kimton N. Eng
Registration No. 43,605

KNE:mco

Enclosures:

Addendum A
Exhibit A

1420 Fifth Avenue, Suite 3400
Seattle, Washington 98101-4010
(206) 903-8800 (telephone)
(206) 903-8200 (fax)
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ADDENDUM A

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

REVOCATION AND SUBSTITUTE POWER OF ATTORNEY

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

In the matter of the patent application identified in Exhibit A attached hereto, I, PAUL A. REVIS, declare that I am a duly authorized designee of Micron Electronics, Inc., the ASSIGNEE of the entire right, title and interest in and to the patent application identified in Exhibit A attached hereto. Pursuant to 37 C.F.R. § 3.73, I certify that Documentary evidence of chain of title from the original owner to ASSIGNEE has been or is concurrently being filed with and recorded by the United States Patent Office. The evidentiary documents referred to in the instant Revocation and Power of Attorney have been reviewed by the undersigned, and it is certified that, to the best of ASSIGNEE's knowledge and belief, title is held solely in and by ASSIGNEE.

On behalf ASSIGNEE, I revoke all power of attorney heretofore given, and hereby appoint EDWARD W. BULCHIS, Reg. No. 26,847; JON F. TUTTLE, Reg. No. 25,713; PAUL T. MEIKLEJOHN, Reg. No. 26,569; GLENN P. RICKARDS, Reg. No. 29,428; DALE C. BARR, Reg. No. 40,498; KIMTON N. ENG, Reg. No. 43,605; DAVID E. BOONE, Reg. No. 27,857; SCOTT W. DOYLE, Reg. No. 39,176; REED R. HEIMBECHER, Reg. No. 36,353; JOHN T. KENNEDY, Reg. No. 42,717; GREGORY D. LEIBOLD, Reg. No. 36,408; GARY M. POLUMBUS, Reg. No. 25,364; THOMAS H. YOUNG, Reg. No. 25,796; W. ROBINSON H. CLARK, Reg. No. 41,530; GREGORY J. GLOVER, Reg. No. 34,173; JOHN K. HARROP, Reg. No. 41,817; CHRIS McWHINNEY, Reg. No. 42,875;

ALDO NOTO, Reg. No. 35,628; MATTHEW PHILLIPS, Reg. No. 43,403; JOHN W. RYAN, Reg. No. 33,771; AMIP. SHAH, Reg. No. 42,143; SEAN S. WOODEN, Reg. No. 43,997; MICHAEL C. GILCHRIST, Reg. No. 40,619; BRIAN J. LAURENZO, Reg. No. 34,207; SHANE COLEMAN, Reg. No. 44,623; RONALD J. BROWN, Reg. No. 29,016; DAVID E. BRUHN, Reg. No. 36,762; DAVID N. FRONEK, Reg. No. 25,678; JOSEPH-F. HAAG, Reg. No. 42,612; STUART R. HEMPHILL, Reg. No. 28,084; GRANT A. JOHNSON, Reg. No. 42,696; KENNETH E. LEVITT, Reg. No. 39,747; NIALA A. MACLEOD, Reg. No. 41,963; SCOTT A. MARKS, Reg. No. 44,902; DEVAN V. PADMANABHAN, Reg. No. 38,262; GERALD H. SULLIVAN, Reg. No. 36,311; BRIAN PARK, Reg. No. P-45,519; MARK W. ROBERTS, Reg. No. P-46,160; STEVEN H. ARTERBERRY, Reg. No. P-46,314; of the firm of DORSEY & WHITNEY LLP, along with HOYT A. FLEMING III, Reg. No. 41,752; and PAUL A. REVIS, Reg. No. 45,040, of Micron Electronics, Inc., 900 East Karcher Road, Nampa, Idaho 83687, as its attorneys to transact all business in the Patent and Trademark Office connected therewith.


Please direct all future correspondence and telephone calls to:

Edward W. Bulchis, Esq.
DORSEY & WHITNEY LLP
U.S. Bank Centre, Suite 3400
1420 Fifth Avenue
Seattle, Washington 98101-4010
(206) 903-8800
(206) 903-8820 facsimile.

ASSIGNEE:

Micron Electronics, Inc.

2/7/10
Date

By 
Paul A. Revis
Intellectual Property Counsel

Enclosure:
Exhibit A

EXHIBIT A

09/083,959	660082.477M	Travis Schaff and Robert Gentile	22-May-98	METHOD AND SYSTEM FOR SELECTING COMPATIBLE PROCESSORS TO ADD TO A MULTIPROCESSOR COMPUTER
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